FIG. 1 PRIOR ART

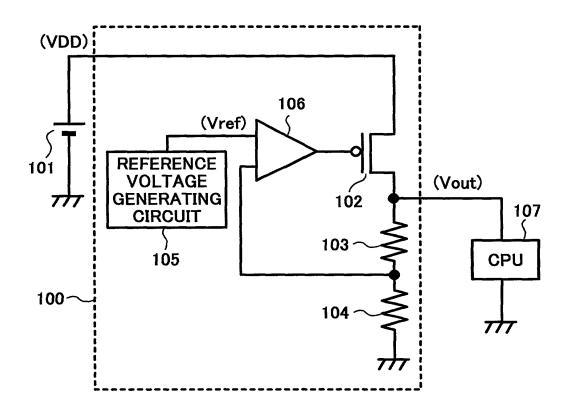


FIG. 2 PRIOR ART

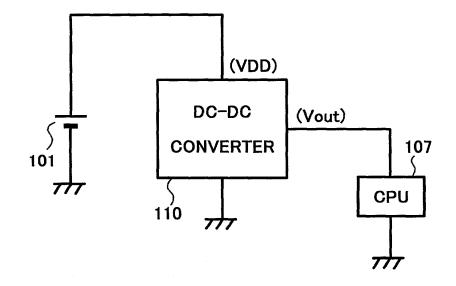


FIG. 3

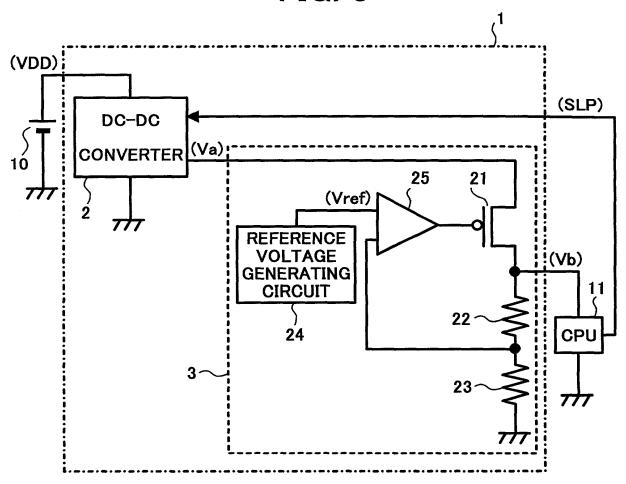


FIG. 4

VDD

31

CONTROLLER

(SLP)

46

777

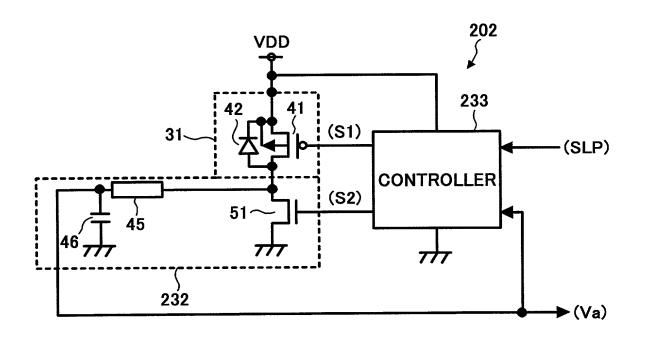
47

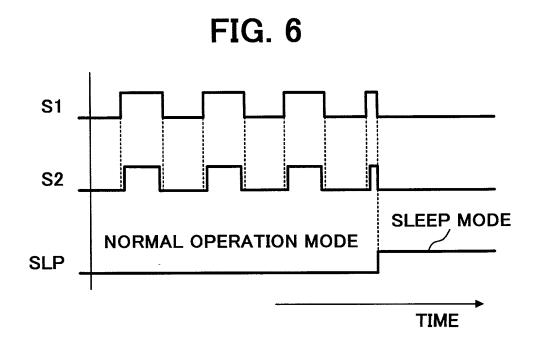
777

32

(Va)

FIG. 5





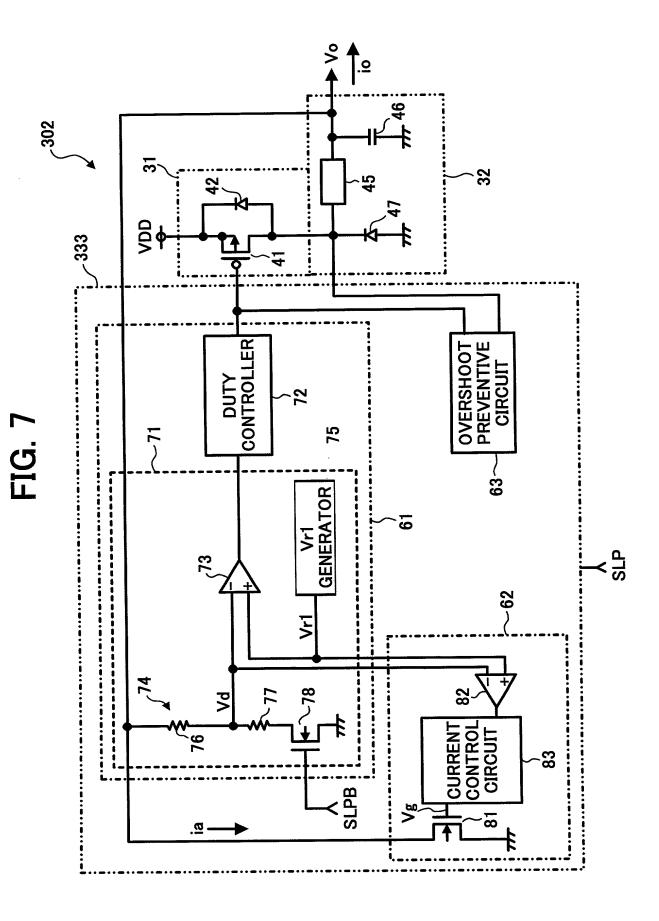


FIG. 8

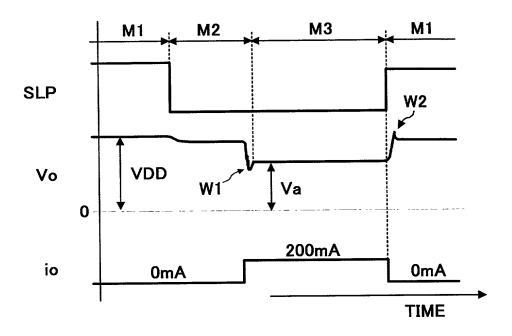


FIG. 9

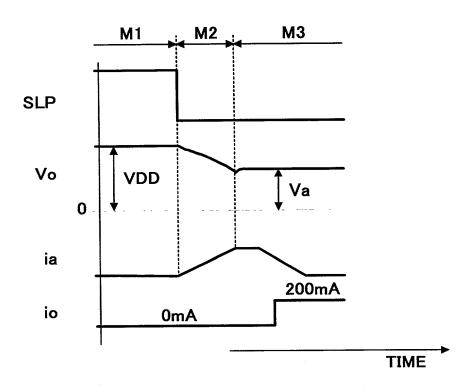


FIG. 10

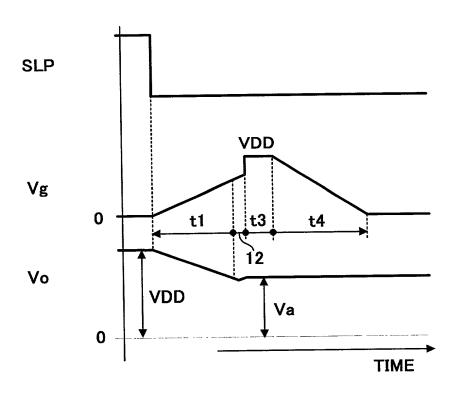


FIG. 11

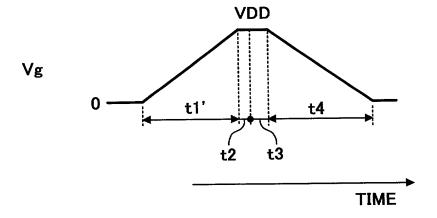
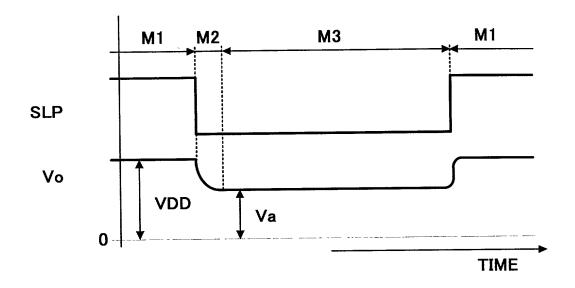


FIG. 12



32 9 .433 OVERSHOOT PROTECTION CIRCUIT DUTY CONTROLLER -91 63~ GENERATOR Vr2 GENERATOR **61**a Vr2 **SWITCHING** SLP SLP CIRCUIT 7 **P** CURRENT CONTROL CIRCUIT 83 SLPB **S** 

FIG. 15

